



RAM Byte	(MSB) (LSB)															
7FH																
2FH									7F	7E	7D	7C	7B	7A	79	78
2EH									77	76	75	74	73	72	71	70
2DH									6F	6E	6D	6C	6B	6A	69	68
2CH									67	66	65	64	63	62	61	60
2BH									5F	5E	5D	5C	5B	5A	59	58
2AH									57	56	55	54	53	52	51	50
29H									4F	4E	4D	4C	4B	4A	49	48
28H									47	46	45	44	43	42	41	40
27H									3F	3E	3D	3C	3B	3A	39	38
26H									37	36	35	34	33	32	31	30
25H									2F	2E	2D	2C	2B	2A	29	28
24H									27	26	25	24	23	22	21	20
23H									1F	1E	1D	1C	1B	1A	19	18
22H									17	16	15	14	13	12	11	10
21H									0F	0E	0D	0C	0B	0A	09	08
20H	07	06	05	04	03	02	01	00								
1FH	Bank 3															
18H									Bank 2							
17H																
10H	Bank 0															
0FH																
08H																
07H																
00																

Direct Byte Address (MSB)	Bit Addresses								Hardware Register Symbol
	(LSB)								
0FFH									
0F0H	F7	F6	F5	F4	F3	F2	F1	F0	B
0E0H									ACC
0D0H	D7	D6	D5	D4	D3	D2	D1	D0	PSW
0B8H	—	—	—	BC	BB	BA	B9	B8	IP
0B0H									P3
0A8H	AF	—	—	AC	AB	AA	A9	A8	IE
0A0H	A7	A6	A5	A4	A3	A2	A1	A0	P2
98H	9F	9E	9D	9C	9B	9A	99	98	SCON
90H	97	96	95	94	93	92	91	90	PI
88H	8F	8E	8D	8C	8B	8A	89	88	TCON
80H	87	86	85	84	83	82	81	80	P0

Figure 2.14 (a) RAM bit addresses. (b) Special function register bit addresses. (Source: Reprinted by permission of Intel Corporation, Copyright © Intel Corporation 1987.)

