

EE 354
Project 2 Status Report

November 9, 2018

Name: _____

Circuit Design

Not Started Half done Complete
 0.....1.....2.....3.....4.....5.....6.....7.....8.....9.....10

Circuit Build

Not Started Half done Complete
 0.....1.....2.....3.....4.....5.....6.....7.....8.....9.....10

Container design

Not Started Half done Complete
 0.....1.....2.....3.....4.....5.....6.....7.....8.....9.....10

Container build

Not Started Half done Complete
 0.....1.....2.....3.....4.....5.....6.....7.....8.....9.....10

Software Design

Not Started Half done Complete
 0.....1.....2.....3.....4.....5.....6.....7.....8.....9.....10

Software Written

Not Started Half done Complete
 0.....1.....2.....3.....4.....5.....6.....7.....8.....9.....10

I am using a printed circuit board Yes No

I am using the 3D printer Yes No