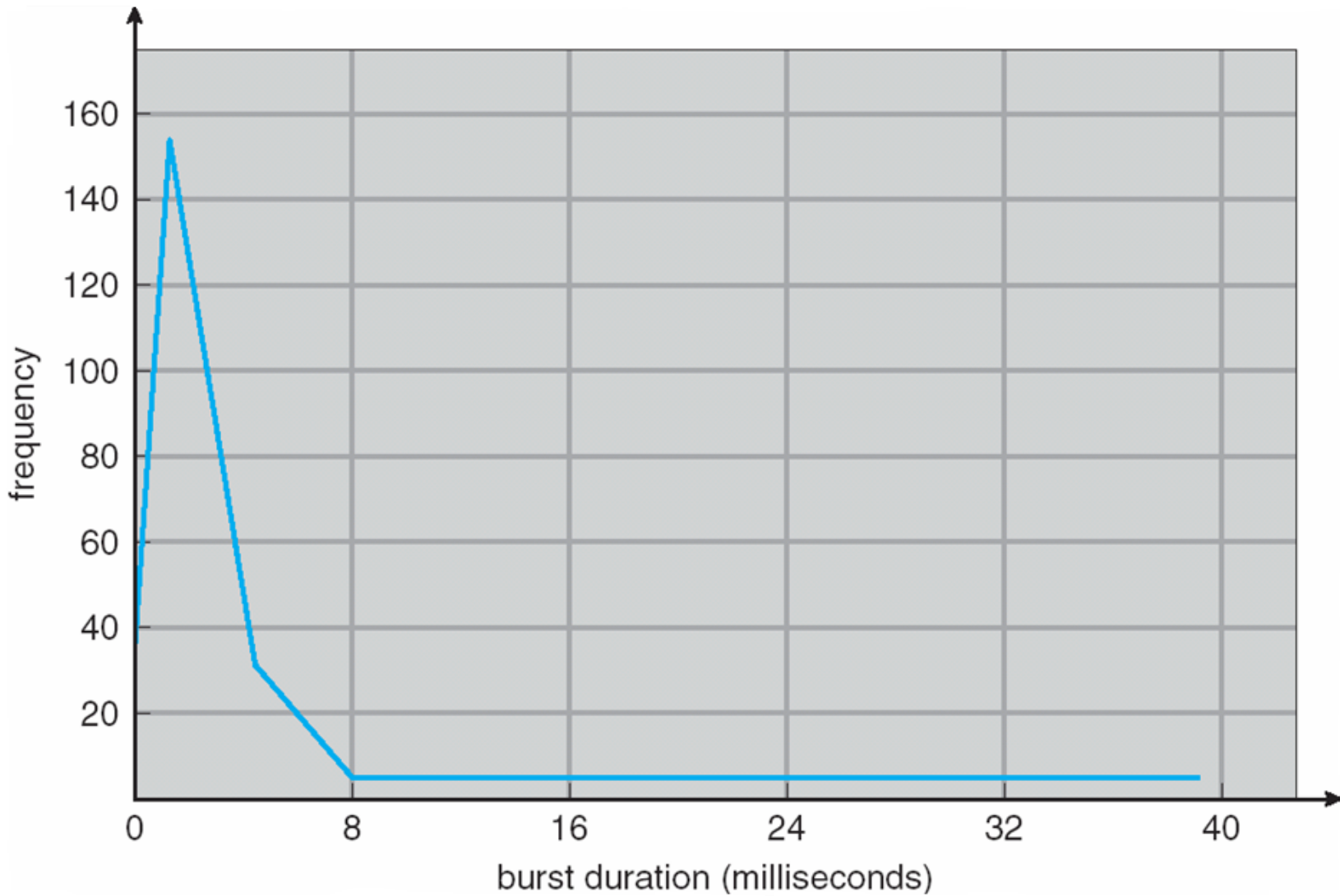
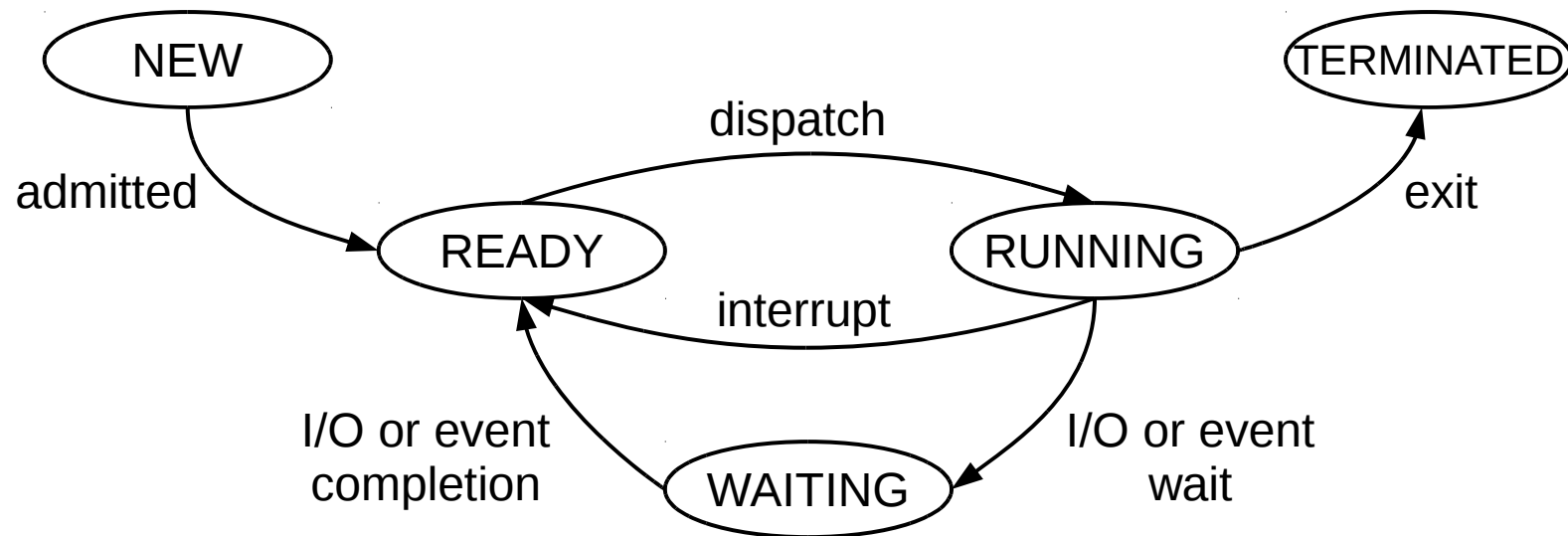


Figure 6.2



Process State Diagram



CPU Scheduling Scenario 1

Process	CPU burst time	Arrival time
p_1	24	0
p_2	3	0
p_3	3	0

with arrival order p_1, p_2, p_3

CPU Scheduling Scenario 2a

Process	CPU burst time	Arrival time
p_1	8	0
p_2	4	0
p_3	9	0
p_4	5	0

CPU Scheduling Scenario 2b

Process	CPU burst time	Arrival time
p_1	8	0
p_2	4	1
p_3	9	2
p_4	5	3